



The Bombay Salesian Society's
Don Bosco Institute of Technology
(An Autonomous Institute Affiliated to University of Mumbai)

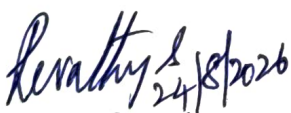
Department of Electronics and Telecommunication Engineering

Date: 24/08/2026

**Time Table for Mid Semester Examination
AY (2026-2027) , ODD SEMESTER**

SE SEMESTER III

Day & Date	Time	Course Code	Course Name
Monday, 31 August 2026	10:00 am - 11:30 am	25ET3PCC03	Digital System Design Using Verilog
Tuesday, 01 September 2026	10:00 am - 11:30 am	25ET3PCC04	Linear Integrated Circuits
Wednesday, 02 September 2026	10:00 am - 11:30 am	25ET3PCC02	Analog and Digital Communication
Thursday, 03 September 2026	10:00 am - 11:30 am	25ET3PCC01	Mathematics for Signal Processing


Dr. Revathy Sundararajan
Controller of Examinations


Dr. Madhavi S. Pednekar
Head of Department


Dr. Sudhakar Mande
Principal